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Docket No.: 60188-031

## **PATENT**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

Kentaro SHIOMI, et al.

Confirmation Number: 6677

Serial No.: 09/779,440

Group Art Unit: 2132

Filed: February 09, 2001

Examiner:

For:

LSI DESIGN METHOD AND VERIFICATION METHOD

## **INFORMATION DISCLOSURE STATEMENT**

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RECEIVED

MAY 0 4 2004

Dear Sir:

**Technology Center 2100** 

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Each non-English language reference was first cited in a corresponding foreign application search report or office action and its relevance discussed therein. A copy of the

09/779,440

foreign search report or office action, together with an English language version thereof, is attached for the Examiner's information.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL, & EMERY

ح.

Michael E. Føgarty

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Date: May 3, 2004

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			APPLICANT Kentaro SHIOMI , et al.							
(PTO-1449)				FILING DATE February 09, 2001			OUP <b>32</b>			
			U.S. PATE	NT DOCUMENTS						
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		, ,	7	no intellectual property protecti 17, pp. 169-173, June 1, 1998.						
YOSHIMORI et al., "VSI Alliance and Technologies for IP Core Application." Toshiba Review, Toshiba Corporation, pp.27-30, Vol. 52, No. 12, December 1, 1997 with partial English Translation thereof.										
CURTIS, Kevin. " Core Proliferation: Security and Protection Issues with IP Integration." Electronic Design, pp. 108- 112, January 12, 1998										
EXAMINER					DATE CONSIDERED					

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.